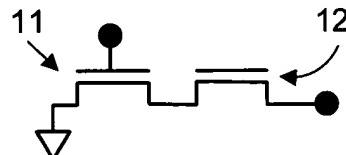


PRIOR ART
Figure 1



PRIOR ART
Figure 1A

SG EE	Erase	Program	Read
SG Wordline (WL)	-17 V (-12 V)	V_t	V_{ref}
Bitline (BL)	V_{ss} (5V)	12 V	V_{dd}
Source Line (SR)	V_{ss}	V_{ss}	V_{ss}

PRIOR ART
Figure 1B